



Integer products from the CNES-CLS IGS Analysis Center

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The common CNES-CLS team has recently joined the group of the IGS Analysis Centers. The main motivations are to propose an alternative software (CNES POD GINS) and an alternative processing strategy, as well as to participate in the improvement of combined IGS products. During the last two years, our processing strategy has been continuously modified to increase quality and reliability and we deliver today a complete set of fully consistent GPS final products.

Since GPS week 1555, we propose an innovative method to fix phase ambiguities at the zero difference level and we compute the corresponding 30 sec phase-clocks. Fixing the wide-lane ambiguity requires a set of daily satellite biases (Wide lane Satellite Biases) that are computed daily. We provide SP3 Orbits, 30s clk phase clocks files, the corresponding Wide-lane satellite biases. These are at a comparable level of accuracy than other ACs.

The first part of our procedure (including pre-processing, un-differenced wide lane fixing, generation of Wide Lane Satellite biases and un-differenced narrow lane fixing) is dedicated to the generation of pwu corrected and phase fixed un-differenced measurements. In the second part we use these measurements to produce the final orbits and 30 seconds integer clocks.

The GRG satellite clocks keep the integer property of the phase ambiguities. Users of GRG orbits/clocks/WSB can do improved PPP: "INTEGER PPP" of isolated receivers and build continuous receiver phase clock solution (from day to day) for time transfer.